

You need more than an FPGA-Development Board

2-FPGA-Board with free Partitioning Software

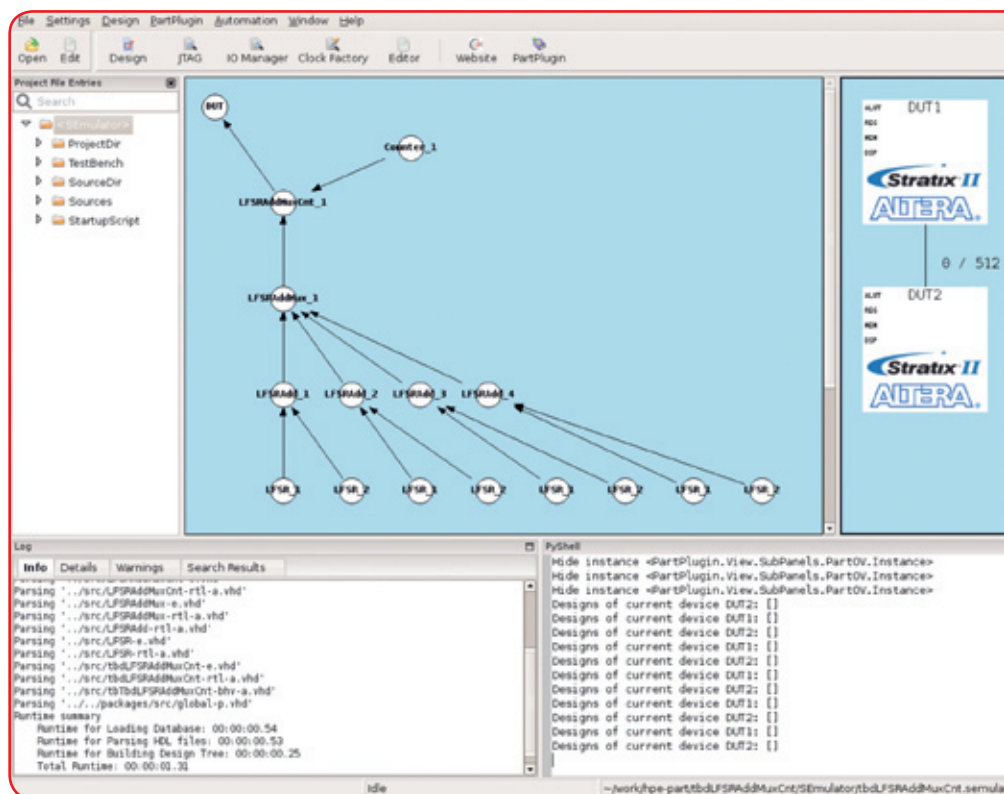
Together with every FPGA-system we will deliver a set of software tools, which are easily accessible via a GUI, called **Hpe®_desk**. In addition to the clock factory the developer will receive a JTAG scanner and debugger. With this scriptable tool it's possible to write any output and to read any input of the FPGA.

Every buyer of a multi FPGA board will get cost-free, semi-automatic partitioning software.

This allows the developer to move every entity from every level by dragging and dropping them into an FPGA. During this task the utilization and the interconnection will be checked.

The tool needs only ALTERA Quartus®II, no additional expensive software will be required.

This picture shows **the working window** of our partitioning tool. You can see the design tree and the 2 FPGA. Every bubble represents an entity. Every entity can be moved into any FPGA, the system check the utilization of the FPGA's and the number of interconnections.



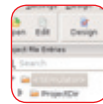
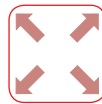
Gleichmann Research offers more high-performance tools

Please ask us for:

- SEmulator®
- Hardware Fault Injector
- AMBA IP Manager

Hotline GE Research

Tel: +43 7236 3351 4599
 Mail: sales@ge-research.com
 www.ge-research.com



Gleichmann Research offers a complete concerted development platform with innovative high quality FPGA development boards and a set of software tools to support the developer.

The times are gone where the developer has to evaluate a different set of tools for each FPGA family, which costs time and money and still these are not perfect.



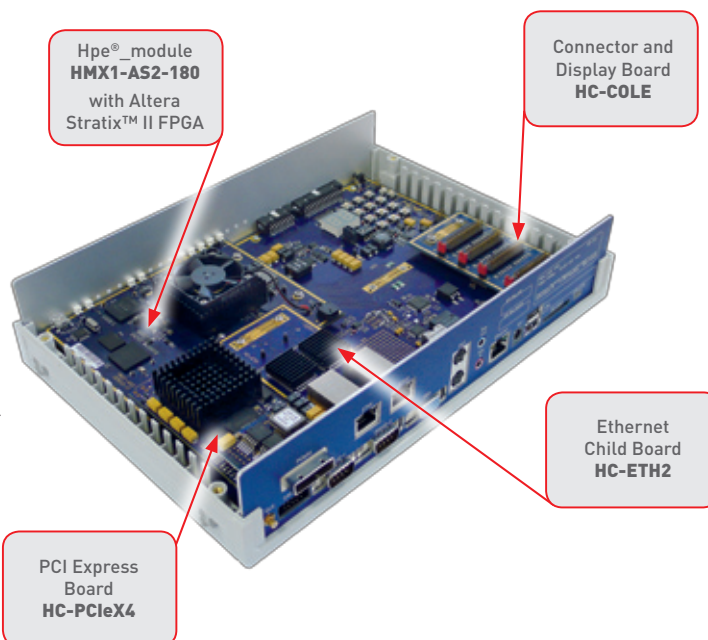
Based on the ALTERA® EP2S180 in a 1508 pin package, Gleichmann Research offers a 2-FPGA-module for the **Hpe®_midi** systems.

Every FPGA has 180.000 logic elements (equivalent to 1.8 Mio. ASIC gate). There are 700 interconnections between the FPGA's, 64 LVDS pairs allow 1Gbit/s/pair high-speed data communication.

A 24-layer PCB, impedance controlled, temperature depended fan control and a clock factory, which distribute 7 input clocks to different clock inputs of the FPGA's – these are some of the features of our FPGA development systems. We have also implemented the ALTERA® USB-Blaster™ on the module.



The **Hpe®_midi** can be used closed from software developer. The hardware developer can use it open with the direct contact and access to the hardware. Many child boards help you to start very fast from the scratch. If you order a child board you will get a customer specific rear panel without any additional charge.



Hpe®_module
HMX1-AS2-180
with Altera
Stratix™ II FPGA

Connector and
Display Board
HC-COLE

Ethernet
Child Board
HC-ETH2

PCI Express
Board
HC-PCIeX4



Gleichmann Electronics Research (Austria) GmbH & Co KG

Softwarepark 37 • A-4232 Hagenberg

Tel: +43 7236 3351 4500 • Mail: sales@ge-research.com • www.ge-research.com